

Impact of Temperature Variation on Resonant Frequency of Active Grounded Inductor-based Bandpass Filter

Shrey Khanna, Debosmit Majumder, Vikash Kumar*, Santashraya Prasad and Aminul Islam

Department of Electronics and Communication Engineering, Birla Institute of Technology (Deemed University),
Mesra, Ranchi - 835215, Jharkhand, India;
shrey10483.12@bitmesra.ac.in, debosmit10378.12@bitmesra.ac.in, vikashkr@bitmesra.ac.in,
s.prashad@bitmesra.ac.in, aminulislam@bitmesra.ac.in

Abstract

Objective: This paper proposes an electronically tunable active grounded inductor circuit using VDVTA as a new active element. The circuit can replace spiral passive inductors for all Analog Signal Processing and data communications operations. **Method/Analysis:** The impact of temperature variation on its resonant frequency is investigated using Virtuoso Analog Design Environment of Cadence @ 45-nm CMOS technology node. **Findings:** This paper carries out a study on CMOS realization of Voltage Differencing Voltage Transconductance Amplifier (VDVTA) and its application as an active grounded inductor. A Bandpass filter circuit is realized using the VDVTA based grounded inductor. Furthermore, the impact of temperature variation on the response of the bandpass filter is analyzed and presented. **Novelty/Improvement:** The proposed circuit uses only active elements and a grounded capacitor. Due to this, the circuit finds applications in all canonical operations and integrated circuit implementations.

Keywords: Active Inductor, Bandpass Filter, Voltage Differencing Voltage Transconductance Amplifier (VDVTA)

1. Introduction

There is a wide range of applications of CMOS spiral inductors in high speed Analog Signal Processing and data communications. The limitations of these spiral inductors include a low-quality factor, a small and non-tunable inductance, a low self-resonant frequency and the need for a large silicon area¹. The passive spiral inductors also cannot scale with the process technology. However, the active inductors have a large tunable quality factor, large tunable inductance and high self-resonant frequency. The cost of production of active inductors are also too less and they are fully compatible with digital CMOS technologies. Hence, realization of active inductor has become a popular research topic in research community to aid design engineer.

Several circuit topologies of various active elements such as operational amplifiers²⁻⁴, current feedback operational amplifier (CFOA)^{5,6}, voltage differencing

buffered amplifiers (VDBA)⁷, current conveyor (CCI)⁸, current differencing transconductance amplifier (CDTA)⁹ were proposed in the literature for realizing active inductor circuits. But these circuits suffer from one or more problems use of external passive resistors, need of some floating passive components, cannot be tuned electronically, etc. In this paper, a new active element namely voltage differencing voltage transconductance amplifier (VDVTA) is used as a building block for realizing active grounded inductor¹⁰⁻¹². VDVTA consists of a current source controlled by the different of two input voltages and a multiple output amplifier which provides electronic tunability by varying its transconductance gain.

VLSI circuits often operate at elevated temperature due to heat generation. Moreover, the temperature of VLSI chip does not remain same throughout entire chip. Hot-spots are found to exist due to variation in activity. The circuit block which is more active gets heated and its temperature is raised generating hot-spot. Due to

* Author for correspondence

generation of hot-spot and variation of temperature, the circuit behavior varies. In an integrated circuit, billions of chips exist. If the performance of chip varies, then the entire integrated circuit performs poorly. This paper investigates the impact of temperature variation on circuit response when VDVTA is used as grounded inductor for realizing bandpass filter.

2. CMOS Realization of Voltage Differencing Voltage Transconductance Amplifier

A symbolic representation of VDVTA is shown in Figure 1. VDVTA contains p, n, v as high impedance input terminals and z, x⁺, x⁻ as high impedance output terminals. VDVTA is characterized by the following matrix:

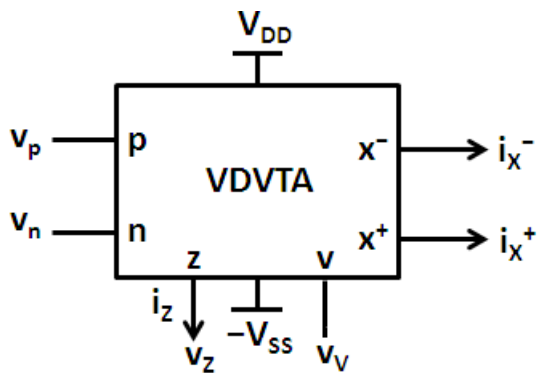


Figure 1. Voltage differencing voltage transconductance amplifier (VDVTA) block diagram.

$$\begin{bmatrix} i_z \\ i_{x+} \\ i_{x-} \end{bmatrix} = \begin{bmatrix} g_{m1} & -g_{m1} & 0 \\ 0 & 0 & g_{m2} \\ 0 & 0 & -g_{m2} \end{bmatrix} \begin{bmatrix} v_p \\ v_n \\ v_z - v_v \end{bmatrix} \quad (1)$$

The port equations obtained from the aforementioned matrix are

$$i_z = g_{m1}(v_p - v_n), \quad (2)$$

$$i_{x+} = g_{m2}(v_z - v_v) \quad (3)$$

$$i_{x-} = -g_{m2}(v_z - v_v). \quad (4)$$

where, g_{m1} and g_{m2} are the two Arbel-Goldminz transconductances (AGTs) given in¹³.

Figure 2 represents a CMOS realization of VDVTA with the terminal 'v' grounded¹³. AGT-I and AGT-II denote positive and negative transconductances respectively.

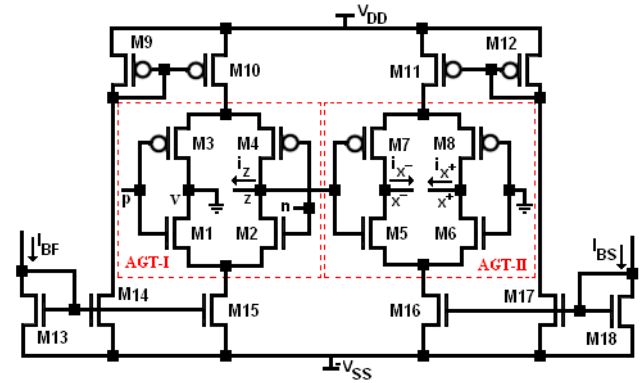


Figure 2. CMOS realization of a voltage differencing voltage transconductance amplifier (VDVTA).

3. Application of VDVTA as Grounded Inductor

VDVTA based grounded inductor circuit is shown in Figure 3. It consists of one VDVTA and a capacitor (C_1). Based on equations obtained from (2-4) and approximating we get the Input Impedance (Z_{in}) of the circuit as:

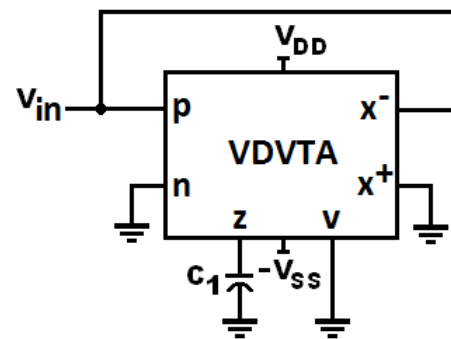


Figure 3. Voltage differencing voltage transconductance amplifier (VDVTA) based grounded inductor.

$$Z_{in} = \frac{sC_1}{g_{m1}g_{m2}}. \quad (5)$$

The equivalent input impedance of the grounded inductor is

$$Z_{in} = sL_{eq} \quad (6)$$

Comparing (5) with (6), the equivalent inductance is found to be

$$L_{eq} = \frac{C_1}{g_{m1}g_{m2}} \quad (7)$$

The inductor obtained from Figure 3 is electronically tunable with both g_{m1} and g_{m2} .

4. Application of VDVTA as Bandpass Filter

The working of the proposed inductor was verified by employing it in a bandpass filter. The bandpass filter circuit realized with VDVTA is shown in Figure 4.

Let the equivalent inductance of the VDVTA configuration is given by 'L'. The expression of resonant frequency of the bandpass filter circuit is given by

$$\omega = \frac{1}{\sqrt{LC}} \quad (8)$$

$$\omega = \sqrt{\frac{g_{m1}g_{m2}}{C_1C_2}} \quad (9)$$

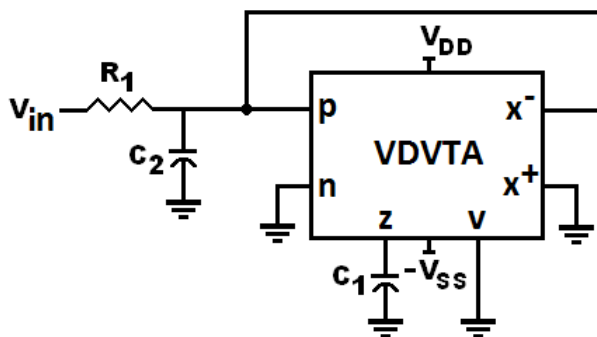


Figure 4. Bandpass filter circuit realized with voltage differencing voltage transconductance amplifier (VDVTA).

4. Results and Discussion

This section presents the AC analysis of bandpass filter which is designed using grounded inductor. The grounded inductor in turn is realized employing voltage differencing voltage transconductance amplifier (VDVTA). The analysis is carried out at different temperatures.

5.1 Variation of Input Impedance of Voltage Differencing Voltage Transconductance Amplifier based Active Grounded Inductor with Frequency

All the simulations in this work were performed using 45-nm industrial CMOS process technology with dual supply of ± 0.9 V. Virtuoso Analog Design Environment of Cadence was used as a simulation tool. Figure 5 shows the input impedance versus frequency plot of the proposed active inductor circuit using VDVTA configuration. The simulated active inductance value was found to be $8.5\mu\text{H}$. As expected the input impedance of the inductor varies linearly with frequency.

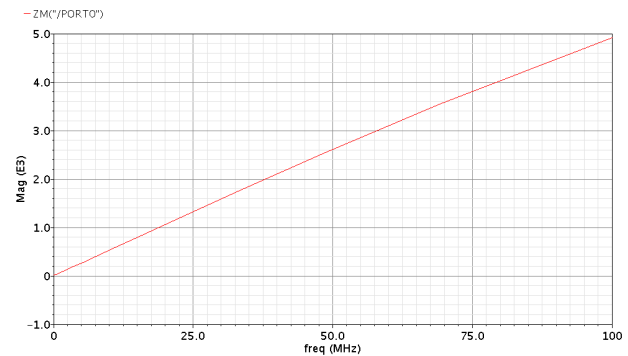


Figure 5. Simulation results showing input impedance variation with frequency.

5.2 Impact of Temperature Variation on Resonant Frequency of Bandpass Filter

The carrier mobility μ_n , and the threshold voltage V_T decrease with temperature. Temperature dependence of V_T is expressed as

$$V_T(T) = V_T(T_0) + \alpha_{V_T}(T - T_0) \quad (10)$$

Where T_0 is the reference temperature and α_{V_T} is a negative constant.

$$\mu_n(T) = \mu_n(T_0) \left(\frac{T}{T_0} \right)^{\alpha_{\mu}} \quad (11)$$

It is observed that the effect of decrease in mobility with temperature dominates and thus the transconductance of a MOSFET decreases with increase in temperature¹⁴.

To verify the temperature dependence of resonant frequency of the bandpass filter circuit, extensive simulations on Virtuoso Analog Design Environment of

Cadence using 45-nm Technology Model are performed. The response of the bandpass filter is analyzed at three different temperatures to observe the departure of resonant frequency from the theoretical values. They are 27°C (room temperature), 40°C, 80°C and 120°C shown in Figure 6. The simulation results are tabulated in Table 1. It is observed that the resonant frequency shifts towards lower frequencies as the temperature increases. This is attributed to the fact that the transconductance of a MOSFET decreases with increase in temperature due to the dominant effect of mobility.

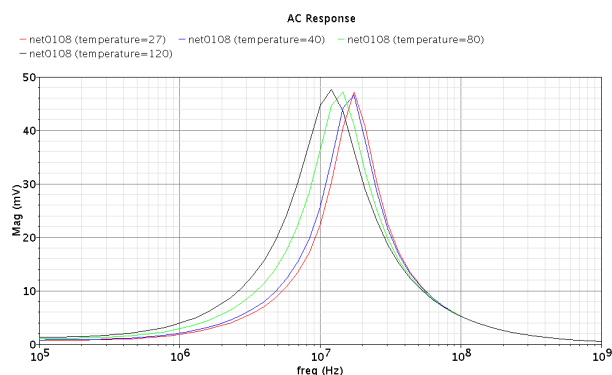


Figure 6. Variation of resonant frequency of Bandpass Filter with temperature. The graph shows decrease of resonant frequency with increase in temperature. It shows $f_r = 17.37$ MHz @ 27°C, $f_r = 16.5$ MHz @ 40°C, $f_r = 13.99$ MHz @ 80°C and $f_r = 12.023$ MHz @ 120°C.

Table 1. Resonant frequency values at various temperatures

Temperature(in °C)	27	40	80	120
Resonant Frequency(in MHz)	17.37	16.5	13.99	12.023

6. Conclusion

An electronically-controllable active grounded inductor circuit was designed based on the voltage differencing voltage transconductance amplifier (VDVTA) configuration using Virtuoso Analog Design Environment of Cadence. Temperature variability analysis was performed to observe the temperature dependence of resonant frequency of the bandpass filter designed using VDVTA based active grounded inductor. The resonant frequency of the active grounded inductor can be electronically tuned. At higher frequencies, the active inductor is subjected to various parasitic effects.

The active inductor circuit finds its application in high-speed analog signal processing and data communication systems.

7. References

1. Yuan F. CMOS active inductors and transformers. New York: Springer; 2008.
2. Ford RL, Girling FEJ. Active filters and oscillators using simulated inductance. Electronics Letters. 1996 Feb; 2(2).
3. Prescott AJ. Loss compensated active gyrator using differential input operational amplifier. Electronics Letters. 1966 Jul; 2(7):283–4.
4. Roy SCD. On operational amplifier simulation of grounded inductance. Archiv fuer Elektronik und Uebertragungstechnik. 1975 Mar; 29:107–15.
5. Yuce E, Minaei S. A modified CFOA and its applications to simulated inductors, capacitance multipliers, and analog filters. IEEE Transactions Circuits and Systems. 2008 Feb; 55(1):254–63.
6. Kacar F, Kuntman H. CFOA-based lossless and lossy inductance simulators. Radioengineering. 2011 Sep; 20(3):627–31.
7. Yesil A, Kacar F, Gurkan K. Lossless grounded inductance simulator employing single VDBA and its experimental band-pass filter application. International Journal of Electronics and Communication (AEU). 2014 Feb; 68(2):143–50.
8. Senani R. Active simulation of inductors using current conveyors. Electronics Letters. 1978 Jan; 14(15):483–84.
9. Prasad D, Bhaskar DR, Singh AK. New grounded and floating simulated inductance circuits using current differencing transconductance amplifiers. Radioengineering. 2010 Apr; 19(1):194–8.
10. Prasad D, Bhaskar DR. Grounded and floating inductance simulation circuits using VDTAs. Scientific Research, Circuits and Systems. 2012 Oct; 3(4):342–7.
11. Gupta G, Singh S, Bhooshan S. VDTA based electronically tunable voltage-mode and trans-admittance biquad filter. Circuits and Systems. 2015 Mar; 6(3):93–102.
12. Arbel AF, Goldminz L. Output stage for current-mode feedback amplifiers, theory and applications. Analog Integrated Circuits and Signal Processing. 1992 Sep; 2(3):243–55.
13. Yesil A, Kacar F, Kuntman H. New simple CMOS realization of voltage differencing transconductance amplifier and its RF filter application. Radioengineering. 2011 Sep; 20(3):632–7.
14. Filanovsky IM, Allam A. Mutual compensation of mobility and threshold voltage temperature effects with applications in CMOS circuits. IEEE Transactions on Circuits and Systems - 1: Fundamental Theory and Applications. 2001 Jul; 48(7):876–84.